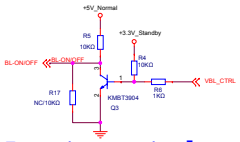
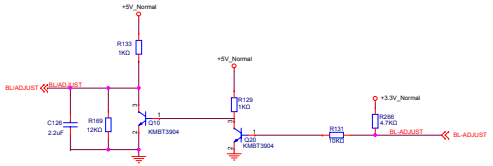


电阻参数更改，位置更改。



Inverter controler

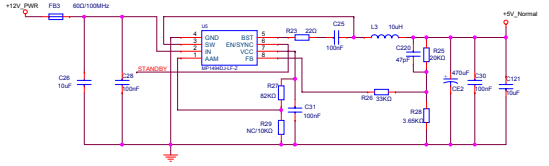




改LG PANEL
掉电屏闪

5、+12V_PWR转+5V_NOR（2A）

MHL做待机充电时，选用1495，无待机充电时，选用1494.



R6	R1	R2	Vout
10K	20K	3.6K	5.0V
33K	11.25K	3.6K	3.3V
33K	5.15K	3.6K	1.5V
33K	3.9K	3.6K	1.2V

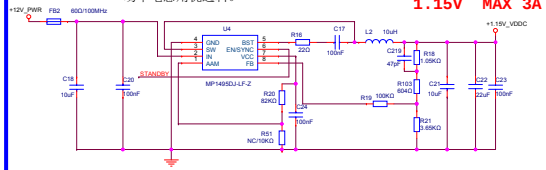
Vout=0.8x(1+(R1/R2))
R1=R2P8
R2=R3P8
Rt=R3P3

说明：1、该电路当前的参数是输出+5V_NOR.
如果要输出其他电压，如3.3V;1.5V;1.2V可以
按照表格里的参数调整R1和R2的大小就可以了
2、PCB排版时输入电容和输出电容尽量靠近IC
PCB排版时储能电感要尽量靠近IC
3、通过增大输入和输出电容的容量可以减小纹波的大小
选择小ESR值的电容也可以减小纹波的大小

1.15V 2A for VDDC

反馈电阻参数更改。
功率电感用优选料。

1.15V MAX 3A

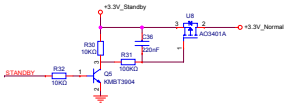


R6	R1	R2	Vout
10K	20K	3.6K	5.0V
33K	11.25K	3.6K	3.3V
33K	5.15K	3.6K	1.5V
100K	1.60K	3.60K	1.15V

Vout=0.8x(1+(R1/R2))
R1=R2P8 Rt=R3P3
R2=R3P8

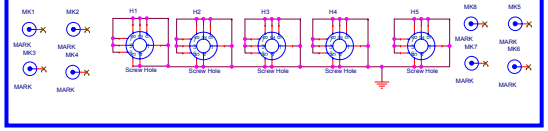
2、PCB排版时输入电容和输出电容尽量靠近IC
PCB排版时储能电感要尽量靠近IC
3、通过增大输入和输出电容的容量可以减小纹波的大小
选择小ESR值的电容也可以减小纹波的大小

5V NORMAL SW(OK)

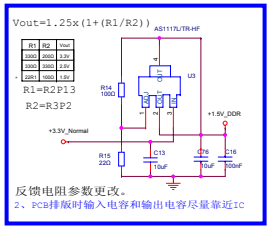


If power input has change,
this part will do option

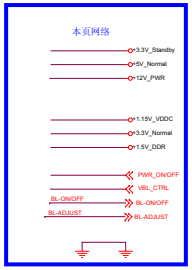
Test Point & MARK(OK)

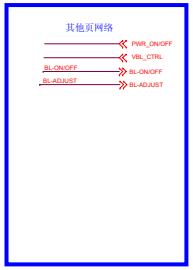


1.5V Power for DDR3(OK)

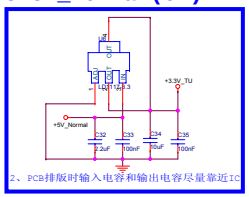


反馈电阻参数更改。
2、PCB排版时输入电容和输出电容尽量靠近IC

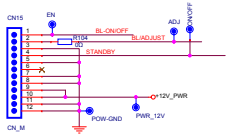




3.3V_Normal(ok)



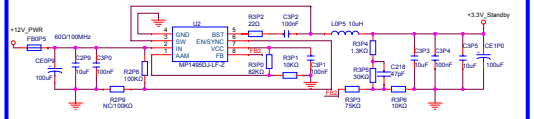
2、PCB排版时输入电容和输出电容尽量靠近IC



5、+12V_NOR转+3.3V_STB（3A）

（DC-DC价格0.13USD 规格标称最大3A 500KHZ 输入电压范围4.5V-16V 轻载高效 优选）

考虑到DC-DC的启开问题，，输出为5V时，最大负载不能超过2.4A



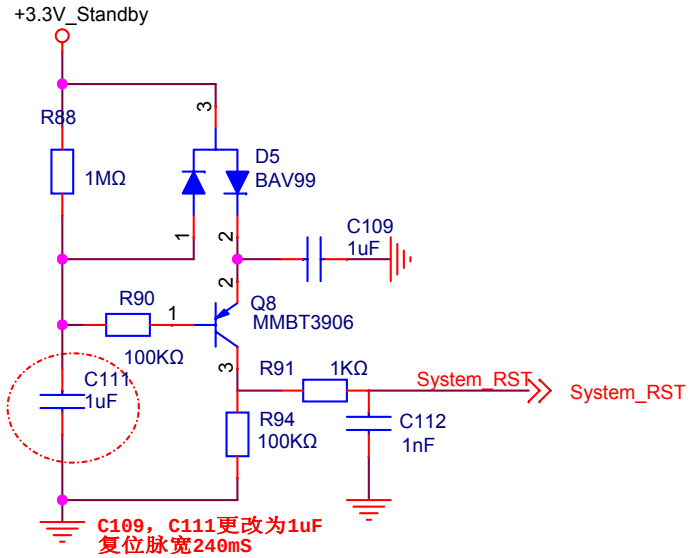
说明：1、该电路当前的参数是输出+5V_NOR. 如果要输出其他电压，如3.3V;2.5V;1.5V可以按照表格里的参数调整R1和R2的大小就可以了
2、PCB排版时输入电容和输出电容尽量靠近IC PCB排版时储能电感要尽量靠近IC
3、通过增大输入和输出电容的容量可以减小纹波的大小 选择小ESR值的电容也可以减小纹波的大小
4、输入输出电容的材质推荐使用X5R或者X7R,不能使用Y5V材质,注意耐压值.
5、模块成本仅供参考,受元器件价格及汇率影响会有变化(成本统计时间20140103)
6、EN端的电压根据实际使用的需要可以进行调整

Vout=0.807x(1+(R1/R2))
R1=R3P4+R3P5 R2=R3P6 Rt=R3P3 L=L0P5

R1(推荐编号)	R2(推荐编号)	R3(推荐编号)	Vout	L(推荐编号)	最大负载	输入电压
R10S-CA3135-2200[1]0[2]+R10S-CA3825-2200[1].0[2]	R10S-CA3135-2201[1]0[2]	R10S-CA4530-2201[1]75[2]	5.0V	R10S-CA3135-4R7[1]0[2]	2.4A	
R10S-CA3030-2200[1]20[2]+R10S-CA3125-2200[1].20[2]	R10S-CA3135-2201[1]0[2]	R10S-CA4530-2201[1]75[2]	3.3V	R00S-3115K-4R7[1]0[2]	3A	1.365RMB
R10S-CA3030-2200[1]20[2]+R10S-CA3125-2200[1]20[2]	R10S-CA3135-2201[1]0[2]	R10S-CA4530-2201[1]75[2]	2.5V	R00S-3123K-3R3[1]0[2]	2.6A	
R10S-CA3030-2200[1].20[2]+R10S-CA3125-2200[1].20[2]	R10S-CA3135-2201[1]0[2]	R10S-CA4530-2201[1]75[2]	1.8V	R00S-3123K-3R3[1]0[2]	2.7A	
R10S-CA3030-2200[1].30[2]+R10S-CA3125-2200[1].30[2]	R10S-CA3135-2201[1]0[2]	R10S-CA4530-2201[1]75[2]	1.5V	R00S-3123K-3R3[1]0[2]	2.6A	1.36RMB
R10S-CA3120-2200[1].20[2]+R10S-CA3625-2200[1]0[2]	R10S-CA3135-2201[1]0[2]	R10S-CA4530-2201[1]75[2]	1.2V	R00S-3123K-3R3[1]0[2]	2.9A	

File	SYSTEM POWER		
Size	Document Number		Rev
D			1.0
Date:	Wednesday, September 17, 2014	Sheet 1 of 13	

RESET



本页网络

System_RST → System_RST

其他页网络

System_RST → System_RST

Title <Title>		
Size A	Document Number <Doc>	Rev <RevCode>
Date: Tuesday, May 06, 2014	Sheet 1 of 1	

video

RGB Interface

VGA

YPBPR

AV Interface

HDMI & Audio

Audio Line IN

Audio Line Out

NAND & CI & TS & Front End

GPIO & LVDS

POWER&GND

Mode Selection

Crystal

NOTE:晶体两端需要各一个30PPM

CLKOUT0/1/3

C1

detect NAND Flash Power

IR&Key_CONNECT

使用物料5400-991128-0800，请注意封装是否相同

RF

Other

DEBUG

heat sink

Standby Power 3.3V

Normal Power 3.3V

CORE POWER

DDR3 POWER

nodie

Close to MST IC

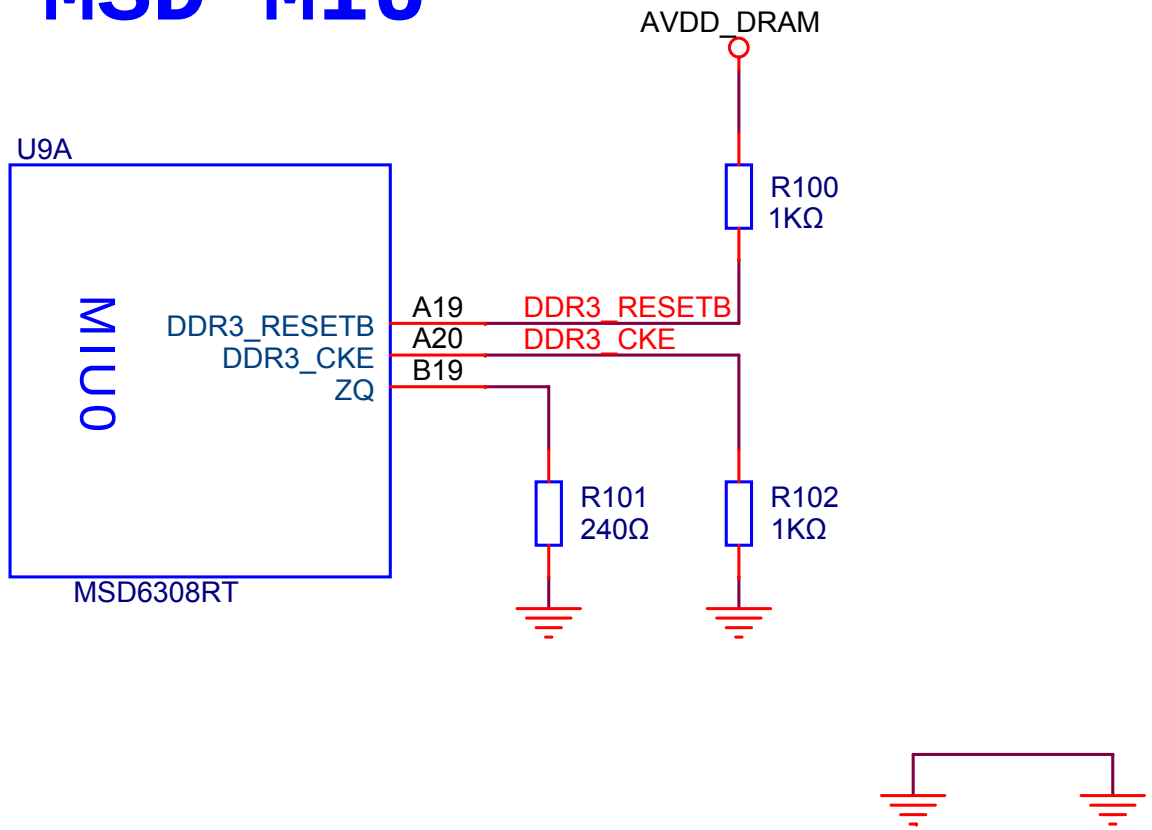
Close to MSTAR IC with width trace

Close to MSTAR IC

Close to MST IC with wide trace

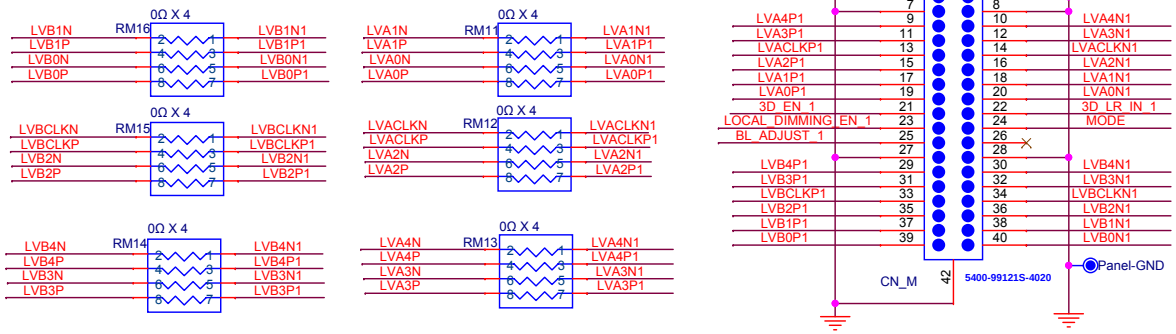
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Date	Sheet of

MSD MIU

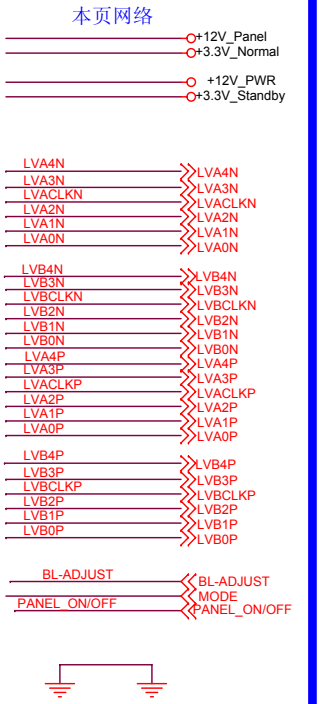


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Date: Wednesday, September 17, 2014	Sheet 3 of 13	

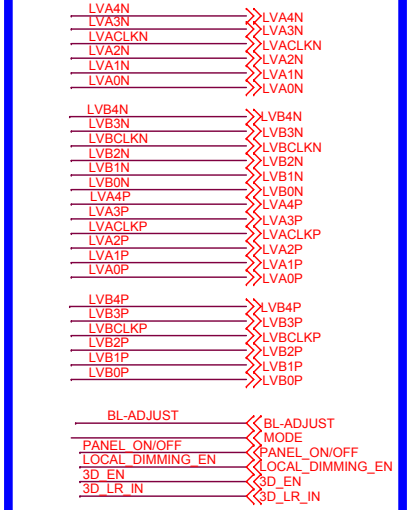
SZ LVDS Connector

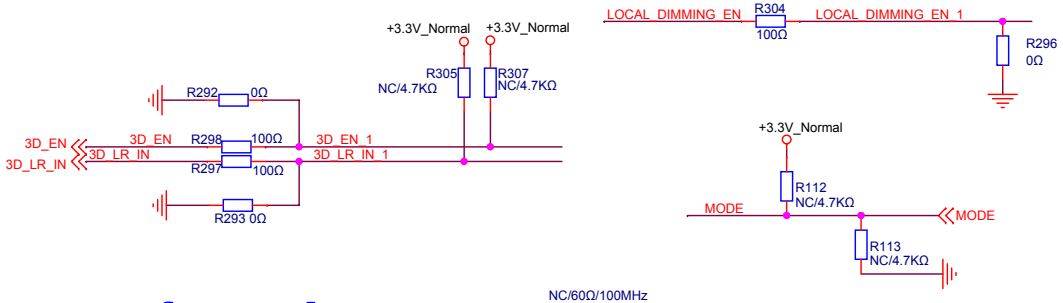


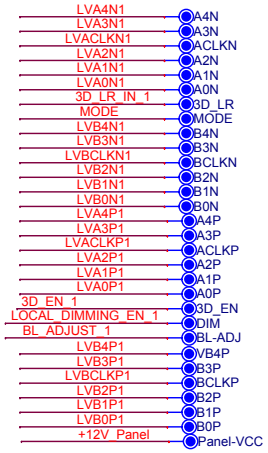
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其他页网络

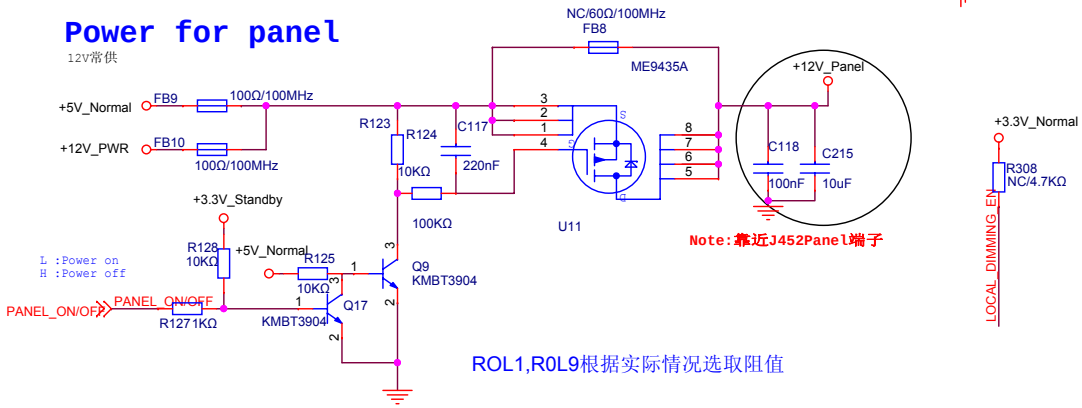






Power for panel

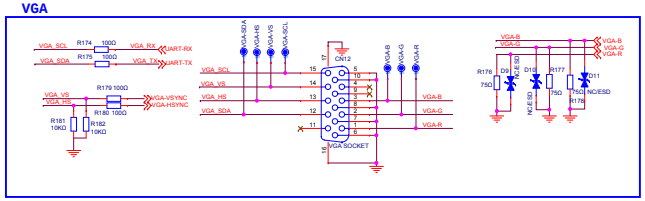
12V常供



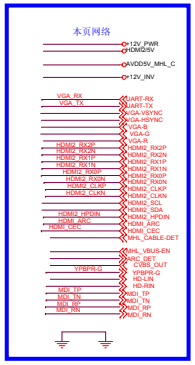


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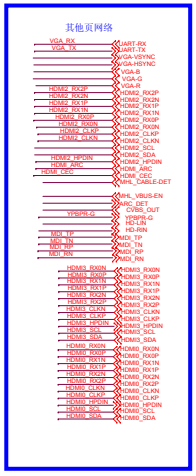
VGA



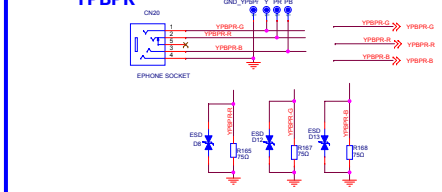
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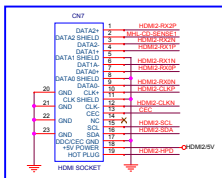
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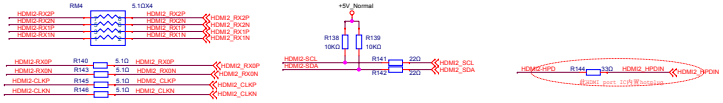
YPBPR



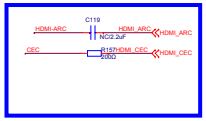
MHL HDMI1



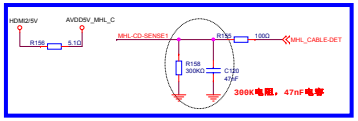
靠近IC放置(OK)



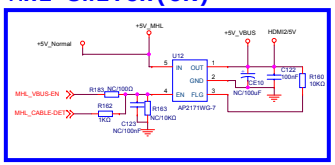
CEC & ARC



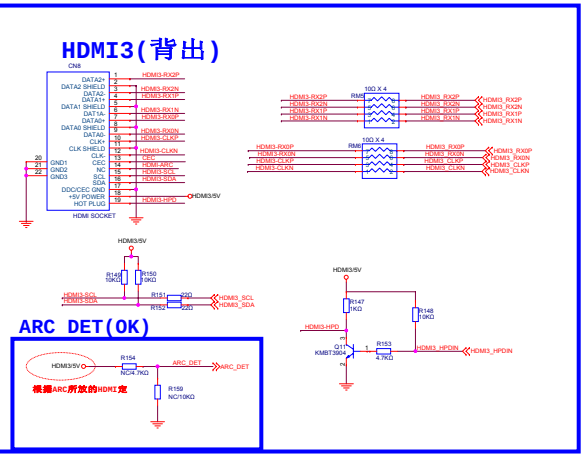
MHL



MHL SWITCH(OK)

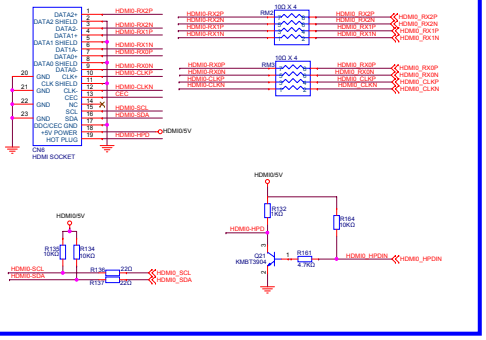


HDMI3(背出)

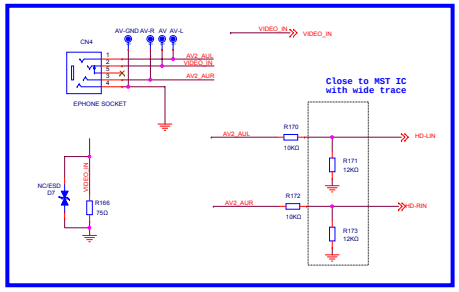


ARC DET(OK)

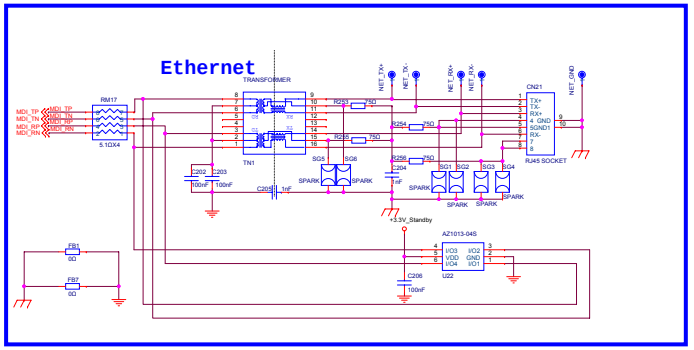
HDMI2

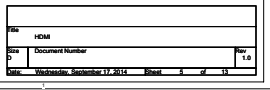


AV-IN(OK)



Ethernet





USB POWER

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+5V_Normal +5V_USB

USB1_DM USB1_DP → USB1_DM USB1_DP

USB0_DM USB0_DP → USB0_DM USB0_DP

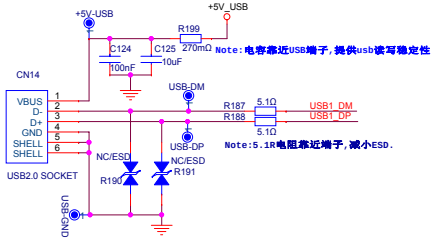
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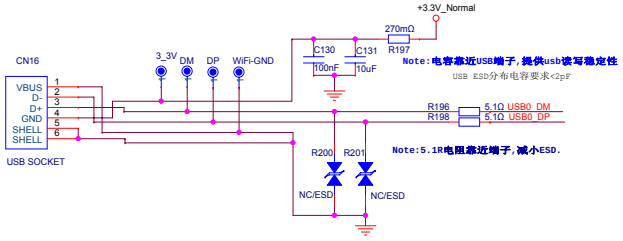
USB1_DM USB1_DP → USB1_DM USB1_DP

USB0_DM USB0_DP → USB0_DM USB0_DP

+3.3V_Normal

USB2.0 INTERFACE





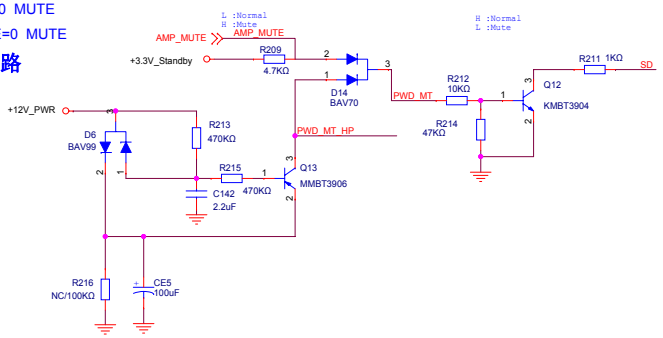
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Date: Wednesday, September 17, 2014	Sheet 8 of 13	

MUTE CONTROL

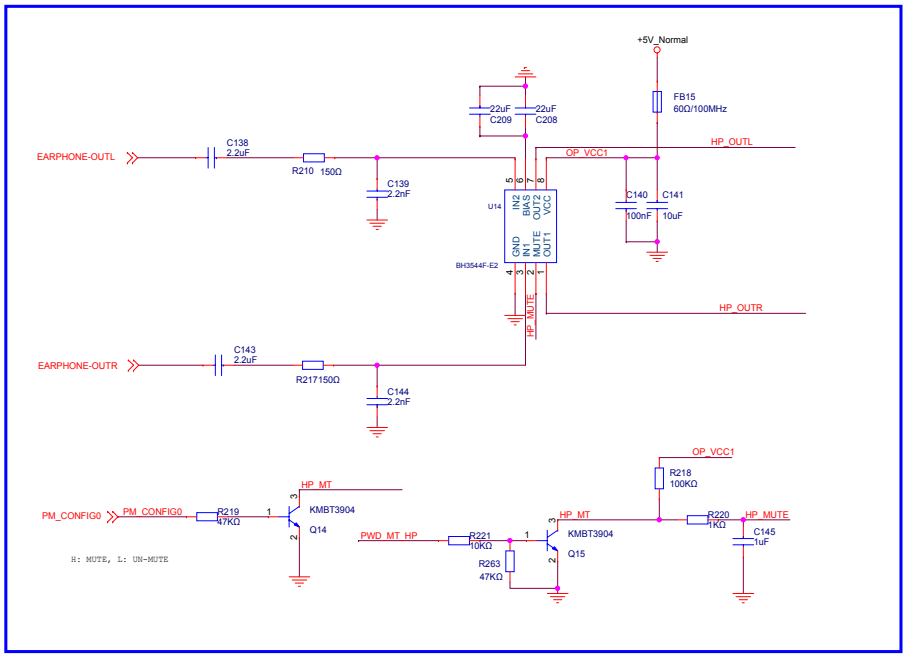
AMP_RST=0 MUTE

AMP_MUTE=0 MUTE

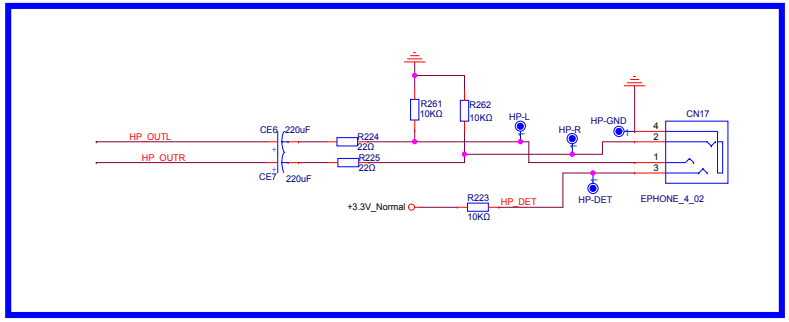
静音电路



HeadPhone Out



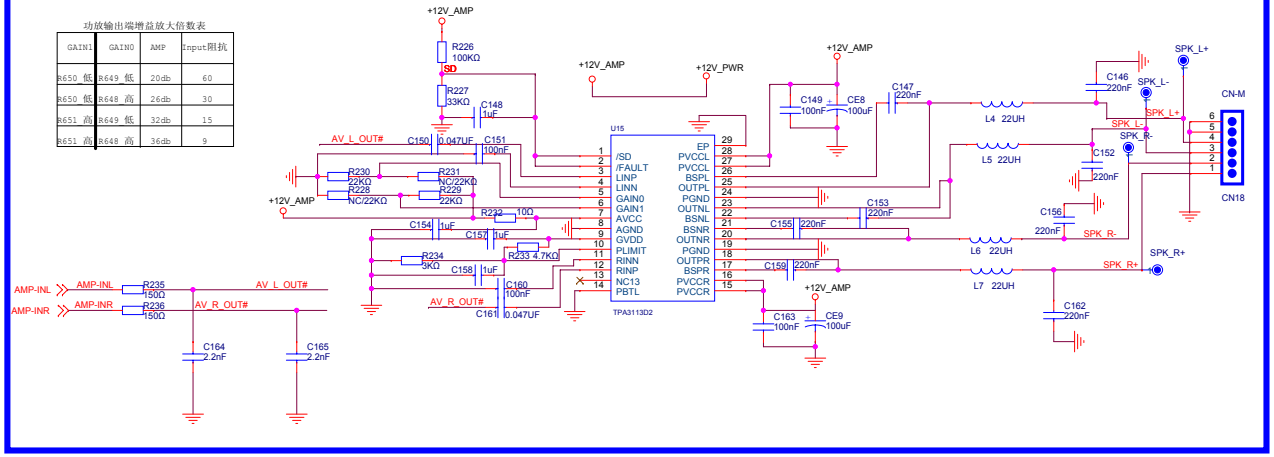
EARPHONE OUT



AUDIO AMP

功放输出端增益放大倍数表

GAIN1	GAIN0	AMP	Input阻抗
R650 低	R649 低	20db	60
R650 低	R648 高	26db	30
R651 高	R649 低	32db	15
R651 高	R648 高	36db	9



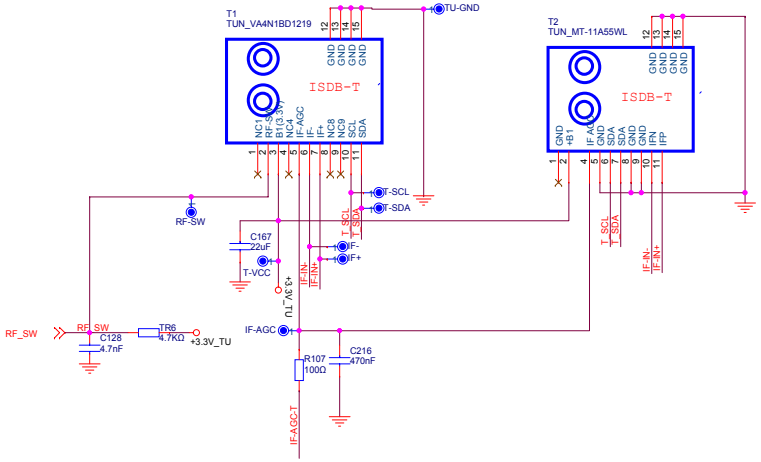
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+3.3V_Standby
+12V_PWR
+3.3V_Normal
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AMP_MUTE
HP_DET
HP_DET
EARPHONE-OUTL
EARPHONE-OUTR
PM_CONFIG0
AMP-INL
AMP-INR

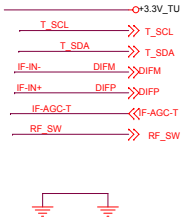
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AMP_MUTE
AMP_MUTE
HP_DET
HP_DET
EARPHONE-OUTL
EARPHONE-OUTR
PM_CONFIG0
AMP-INL
AMP-INR

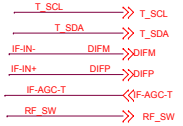
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Size C	Document Number	Rev 1.0
Date: Wednesday, September 17, 2014	Sheet 9 of 13	



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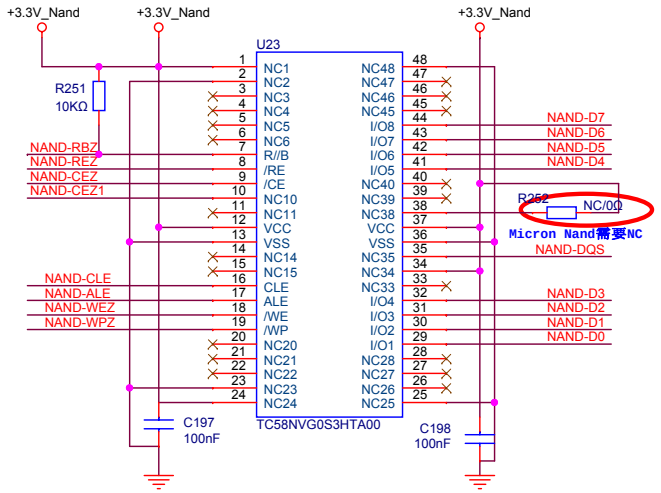


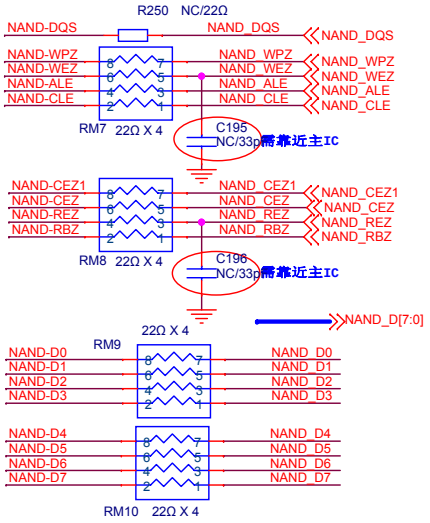
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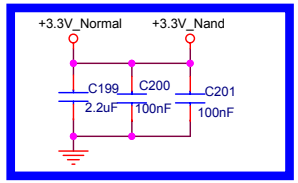


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Size C	Document Number	Rev 1.0
Date: Wednesday, September 17, 2014	Sheet 10 of 13	

NAND FLASH

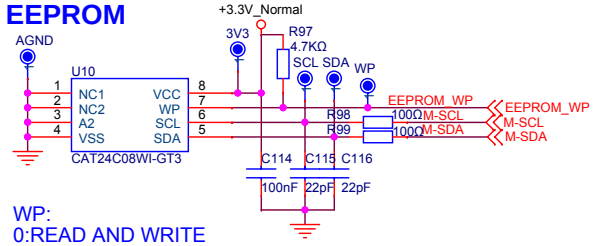






NAND Power

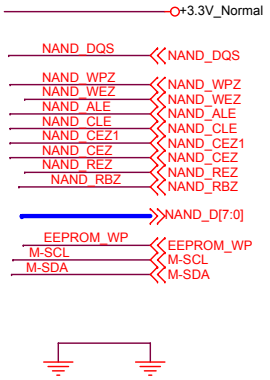
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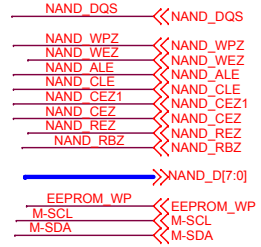
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1::WRITE PROTECT

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其他页网络



Title	NAND FLASH	
Size B	Document Number	Rev 1.0
Date: Wednesday, September 17, 2014	Sheet 11 of 13	